

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Morrison et al.

Serial No.: 10/797,647

Filed: March 10, 2004

For: SEMICONDUCTOR BGA PACKAGE
HAVING A SEGMENTED VOLTAGE
PLANE AND METHOD OF MAKING

Confirmation No.: 4195

Examiner: M. Lewis

Group Art Unit: 2822

Attorney Docket No.: 2269-6103US
(03-0954.00/US)

Notice of Allowance Mailed:

February 9, 2007

**VIA ELECTRONIC FILING
May 8, 2007**

**FEE ADDRESSEE FOR RECEIPT OF PTO NOTICES
RELATING TO MAINTENANCE FEES**

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

1. This letter is to specify that the FEE ADDRESSEE for this patent is:

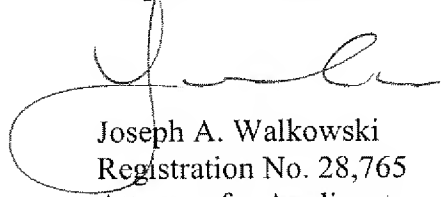
Serial No. 10/797,647

MICRON TECHNOLOGY, INC.

Mail Stop 525
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2. The Customer Number for the Fee Addressee is **26809**.
3. Any prior FEE ADDRESSEE for this patent is hereby revoked.
4. It is certified that the person whose signature appears below has the authority to change the FEE ADDRESSEE for this patent.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'J. Walkowski', with a large, stylized loop at the beginning.

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Date: May 8, 2007
JAW/dlm

Document in ProLaw